

SCWOS 74

-11002 U.S. PRO  
09/975293  
10/11/01

**ISSUE CLASSIFICATION**

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PATENT NUMBER

**U.S. UTILITY** Patent Application

 <b>O.I.P.E.</b> SCANNED  Q.A.	<b>PATENT DATE</b> 
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APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/975293	D	714	763	2133	DeRud

## APPlicants

Max Yeung  
Richard Stephani  
Miguel Vilchis

RSA

**ER**

for me

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## Construction of an optimized SEC-DED code and logic for soft errors in semiconductor memories

PTO-2040  
12/99

## **ISSUING CLASSIFICATION**

□ TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.				<b>NOTICE OF ALLOWANCE MAILED</b>	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____				<b>ISSUE FEE</b>	
				Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.				<b>ISSUE BATCH NUMBER</b>	

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